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(FILE 'HOME' ENTERED AT 16:44:27 ON 22 JUN 2000)

FILE 'USPATFULL' ENTERED AT 16:44:49 ON 22 JUN 2000

L1	2375 S (INTEGRATED CIRCUIT#) (5A) CLOCK
L2	17709 S IMPEDANCE (5A) MATCH###
L3	52 S L1 AND L2
L4	9427 S PROGRAMMABLE LOGIC
L5	2 S L3 AND L4
L6	335 S 716/1/NCL OR 716/2/NCL
L7	398 S 716/5/NCL OR 716/6/NCL
L8	2 S L2 AND L7
L9	6 S L2 AND L6
L10	137 S 716/16/NCL
L11	94 S L10 AND CLOCK#
L12	4 S L1 AND L11
L13	4 S L11 AND PLL